

ABSTRACT OF THE DISCLOSURE

A frame density ratio between a RAM area and a ROM area is set to be a numerical value close to a data density ratio between the RAM area and the ROM area and to be a simple ratio of integers. ROM data is subjected to RLL (2, 7) modulation as is a prepit address of RAM data, and dcc of 6 channel bits is inserted after each unit of the ROM data. Frame sync (FS) of 48 channel bits is added at the front of ROM recording frames, while postamble (PO) is added at the end of the ROM recording frames so as to adjust frame length. Although data of 156 bytes can be recorded in a single ROM recording frame, data of 155 bytes is recorded in the ROM recording frame as in a RAM data frame. Thus, structure of data higher than that in an ECC format is made to be the same as that of the RAM data.

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